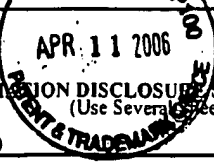


FORM PTO-1449 (Modified)		 APR 11 2006 U.S. Department of Commerce Patent and Trademark Office		Attorney Docket No.: SONY-27300	Serial No.: 10/816,391	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary)				Applicants: Mikhail Dorojevets et al.		
(37 CFR § 1.98(b))				Filing Date: March 31, 2004	Group Art Unit: 2613	

U.S. PATENT DOCUMENTS							
Examiner Initials	Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date	
/A.H./	AA	4,128,880	12/05/78	Cray, Jr.	364	200	06/30/76
	AB						
	AC						
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	AI						
	AJ						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS								
		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AK							
	AL							
	AM							
	AN							
	AO							

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)		
	AP	
	AQ	
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	AV	
	AW	
	AX	
	AY	
	AZ	

Examiner: /Anner Holder/	Date Considered: 12/11/2007
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EXAMINER:	Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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FORM PTO-1449
(Modified)

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U.S. Department of Commerce
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Attorney Docket No.: SONY-27300

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Include Additional Serial Sheets If Necessary)

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(37 CFR § 1.98(b))

Filing Date: March 31, 2004

Group Art Unit: 2613

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/A.H./	AA	4,725,973	2/16/88	Matsuura et al.	364	736	10/25/83
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/A.H./	AP	Donald Blevins and Edward Davis, "A Bit-Serial VLSI Array Processing Chip for Image Processing," pages 364-368, IEEE Journal of Solid-State Circuits, vol. 25, No. 2, April 1990					
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Examiner: /Anner Holder/

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